SPECIFICATION FOR APPROVAL

()	Preliminary S _I	pecification
			_

() Final Specification

Title	15.6" HD TFT LCD
-------	------------------

Customer	Asustek
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LP156WH2
Suffix	TLA1

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
/	
/	
/	
-	
Please return 1 copy for yo	

your signature and comments.

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Jul. 18. 2008	-	First Draft (Preliminary Specification)	-

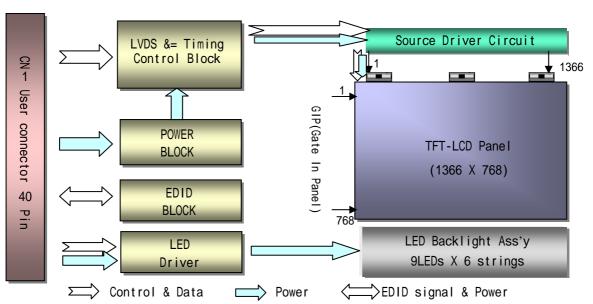


1. General Description

The LP156WH2 is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.6 inches diagonally measured active display area with HD resolution(768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP156WH2 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP156WH2 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP156WH2 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.6 inches diagonal		
Outline Dimension	359.3(H, typ) × 209.5(V, typ) × 5.5(D,max) [mm]		
Pixel Pitch	0.252mm × 0.252 mm		
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement		
Color Depth	6-bit, 262,144 colors		
Luminance, White	220 cd/m ² (Typ.5 point)		
Power Consumption	Total TBD Watt(Typ.) @ LCM circuit TBD Watt(Typ.), B/L input 3.5 Watt(Typ.)		
Weight	450g (Max.)		
Display Operating Mode	Transmissive mode, normally white		
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer		
RoHS Comply	Yes		

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2. Absolute Maximum Ratings

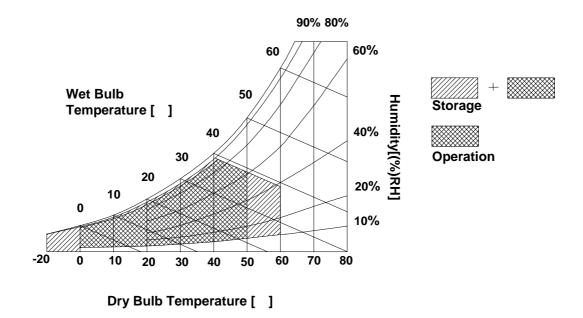
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Offics		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

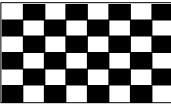
The LP156WH2 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL, is typically generated by an LED Driver. The LED Driver is an internal unit to the LCD.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Unit	Notes			
Faiametei	Symbol	Min	Тур	Max	Offic	Notes	
MODULE :							
Power Supply Input Voltage	VCC	3.0	3.3	3.6	V _{DC}	3	
Power Supply Input Current	I _{cc}	-	TBD	TBD	mA	1	
Power Consumption	Pc	-	TBD	TBD	Watt	1	
Differential Impedance	Zm	90	100	110	Ohm	2	
LED BL :							
Operating Voltage	V_{LED}	-	28.8	30.6	V	3	
Operating Current per String	I _{LED}	-	20	-	mA	4	
Power Consumption	P_{LED}	-	3.5	3.7	Watt	5	
Life Time		15,000	-	-	Hrs	6	

Note)

1. The specified current and power consumption are under the Vcc = 3.3V, 25 , fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The variance of the voltage is \pm 10%.
- 4. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics. I_{LED} is the current of each LEDs' string, LED backlight has 6 strings on it.
- 5. The LED power consumption shown above does not include power of internal LED driver circuit for typical current condition.
- 6. The life time is determined as the time at which brightness of LED is 50% compare to that of minimum value specified in table 7.

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3-2. Interface Connections

This LCD employs one interface connections, a 40 pin connector is used for the module electronics interface and LED Driver.

The electronics interface connector is a model 20455-040E-0x manufactured by I-PEX.

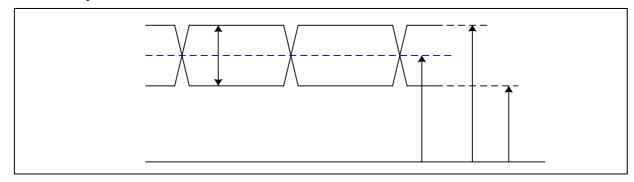
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No connection	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	NC	No Connection	1.1 LCD: SW, SW0624 (LCD Controller)
6	Clk EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	1.2 System : THC63LVDF823A
8	Odd_R _{IN} 0-	Negative LVDS differential data input	or equivalent * Pin to Pin compatible with LVDS
9	Odd_R _{IN} 0+	Positive LVDS differential data input	Till to Fill odinpatible with EVBO
10	GND	Ground	2. Connector
11	Odd_R _{IN} 1-	Negative LVDS differential data input	2.1 LCD :20455-040E-0x, I-PEX or its compatibles
12	Odd_R _{IN} 1+	Positive LVDS differential data input	2.2 Mating : 20453-040T-0x, I-PEX
13	GND	Ground	or equivalent.
14	Odd_R _{IN} 2-	Negative LVDS differential data input	2.3 Connector pin arrangement
15	Odd_R _{IN} 2+	Positive LVDS differential data input	40 1
16	GND	Ground	jjulii j
17	Odd_CLKIN-	Negative LVDS differential clock input	[""
18	Odd_CLKIN+	Positive LVDS differential clock input	
19	GND	Ground	[LCD Module Rear View]
20	NC NC	No Connection	
21	NC NC	No Connection	
22	NC NC	No Connection	
23	NC NC	No Connection	
24	NC NC	No Connection	
25	NC	No Connection	
26	NC	No Connection	
27	NC	No Connection	
28	NC	No Connection	
29	NC	No Connection	
30	NC	No Connection	
31	VLED_GND	LED Ground	
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
1			
34	NC	No Connection	
35	BLIM	PWM for Luminance control	
36	BL_On	Backlight On/Off Control	
37	NC	No Connection	
38	VLED	LED Power Supply (7V-20V)	
39	VLED	LED Power Supply (7V-20V)	
40	VLED	LED Power Supply (7V-20V)	
70	V	225 Tomo: Supply (1 v 20 v)	



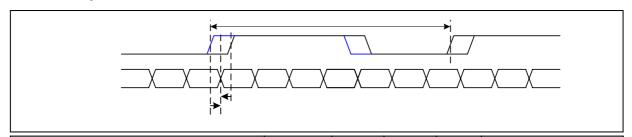
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

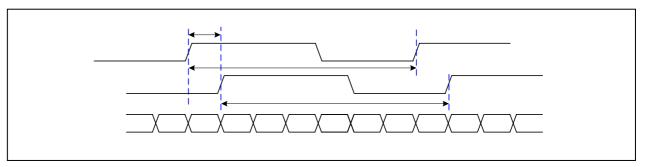
3-3-2. AC Specification



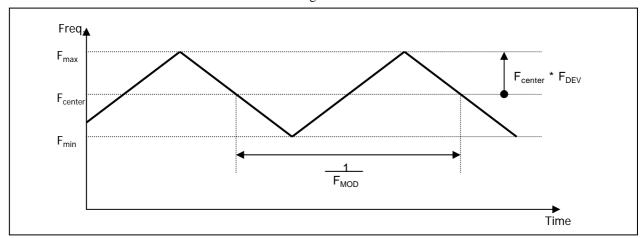
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	Eclk 65MHz
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk 25MHz -
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	VDS +

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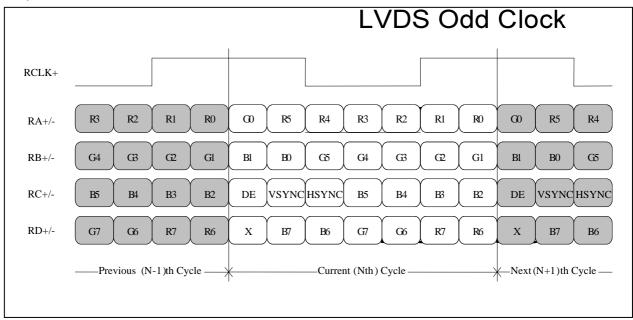
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

Condition: VCC =3.3V



Product Specification

3-4. Signal Timing Specifications

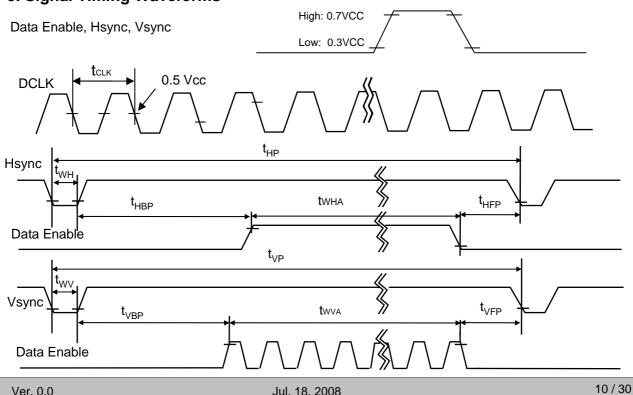
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	72.3	-	MHz	
	Period	t _{HP}	1470	1526	1586		
Hsync	Width	t _{WH}	23	32	40	tCLK	
	Width-Active	t _{WHA}	1366	1366	1366		
	Period	t _{VP}	779	790	801		
Vsync	Width	t _{wv}	2	5	8	tHP	
	Width-Active	t _{WVA}	768	768	768		
	Horizontal back porch	t _{HBP}	72	80	124	+CL IV	
Data	Horizontal front porch	t _{HFP}	8	48	48	tCLK	
Enable	Vertical back porch	t _{VBP}	8	14	20	+UD	
	Vertical front porch	t _{VFP}	1	3	5	tHP	

3-5. Signal Timing Waveforms

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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
	50101	MSE	3				LSB	MSE	3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	.1	1	1		0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																	· · · · · ·		
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					• • • • • • •														
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	 1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	 0	0	0	0	0	0	0	····· 1
BLUE					 														••••
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	 0	0	1	 1		 1	1	
	BLUE (63)	0	0	0	0	0		0	0	0	0	 0	0		 1	1		i. 1	1
	(,													L					



3-7. Power Sequence

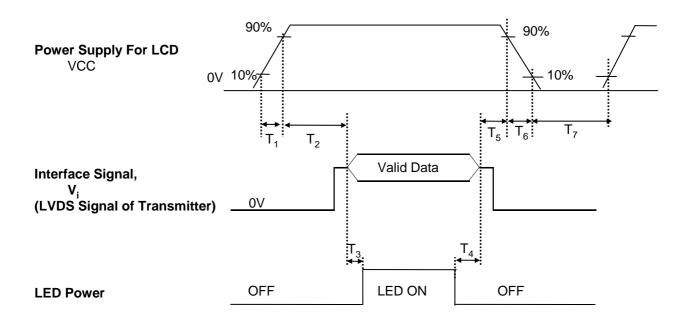


Table 8. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	0	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	3	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. Lamp power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



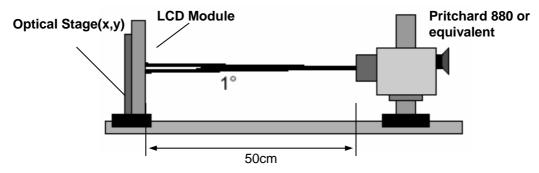


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 72.3MHz, I_{LED} = ???mA

Demonstra	0		Values		I I I I I I I I I I I I I I I I I I I	Neter
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	400	-		l	1
Surface Luminance, white	L_WH	190	220	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6]	3
Response Time	Tr_{R} + Tr_{D}	-	8	-	ms	4
Color Coordinates					1	
RED	RX	TBD	TBD	TBD	1	
	RY	TBD	TBD	TBD	[
GREEN	GX	TBD	TBD	TBD	[
	GY	TBD	TBD	TBD	[
BLUE	BX	TBD	TBD	TBD		
	BY	TBD	TBD	TBD		
WHITE	WX	0.283	0.313	0.343	1	
	WY	0.299	0.329	0.359	<u>.</u>	
Viewing Angle					<u>.</u>	5
x axis, right(Φ=0°)	Θr	40	-	-	degree	
x axis, left (Φ=180°)	Θl	40	-	- 	degree	
y axis, up (⊕=90°)	Θu	10	-	-	degree	
y axis, down (Φ=270°)	Θd	30	-	-	degree	
Gray Scale						6

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Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{ WHITE}} = \frac{\text{Maximum}(\mathsf{L}_1, \mathsf{L}_2, \ \dots \ \mathsf{L}_{13})}{\text{Minimum}(\mathsf{L}_1, \mathsf{L}_2, \ \dots \ \mathsf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_V = 60Hz$$

Gray Level	Luminance [%] (Typ)
LO	0
L7	TBD
L15	TBD
L23	TBD
	TBD
	TBD
L47	TBD
L55	TBD
L63	100

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FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

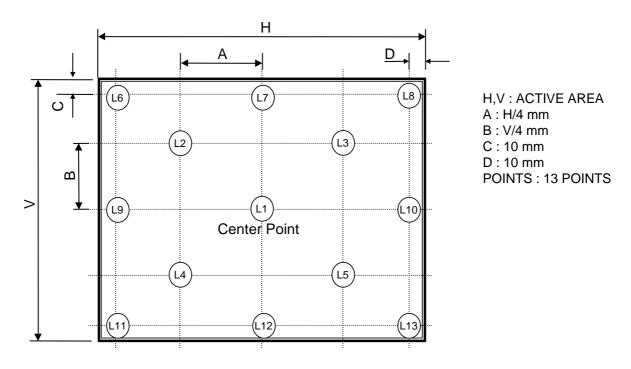
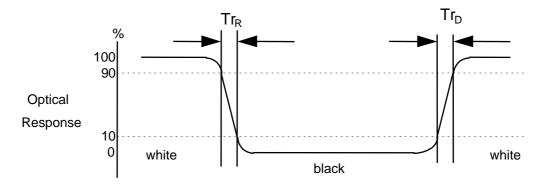


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



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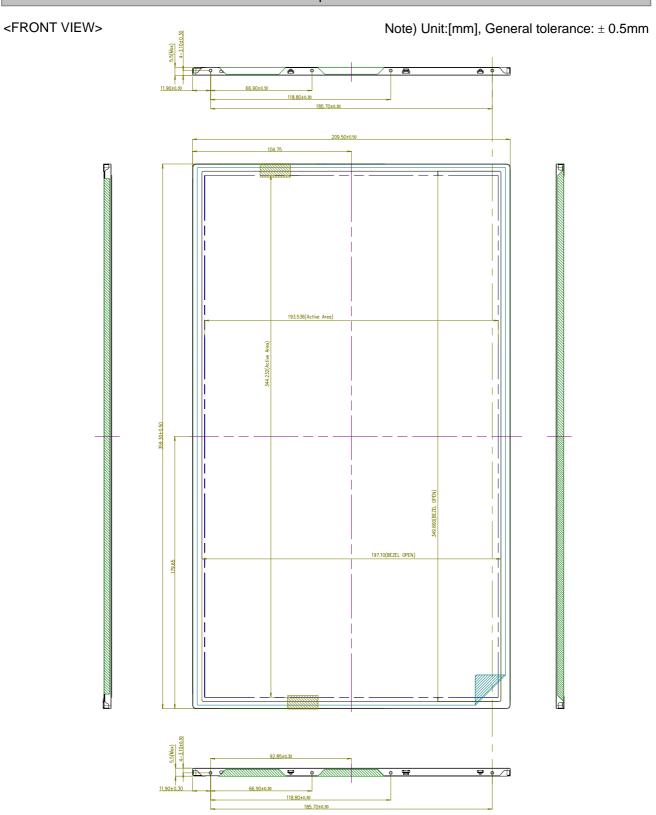


5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP156WH2. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	359.3 ± 0.5mm				
Outline Dimension	Vertical	209.5 ± 0.5mm				
	Thickness	5.5mm (max)				
Bezel Area	Horizontal	349.8 ± 0.5mm				
Dezei Alea	Vertical	197.1 ± 0.5mm				
Active Diepley Area	Horizontal	344.232 mm				
Active Display Area	Vertical	193.536 mm				
Weight	450g (Max.)					
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer					

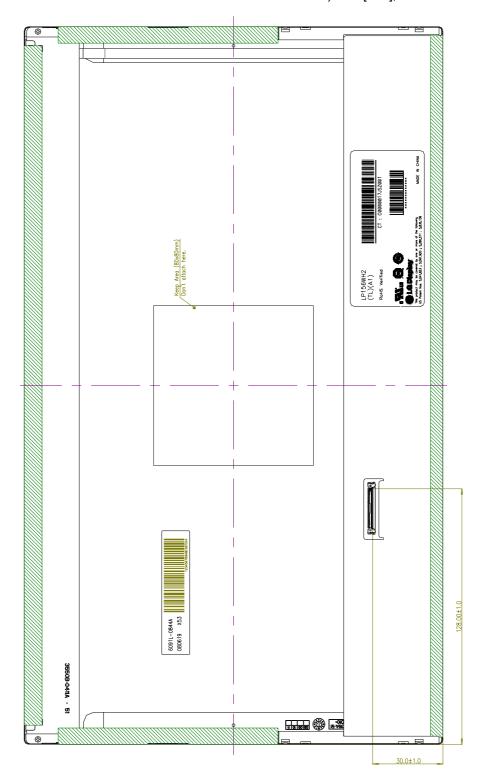






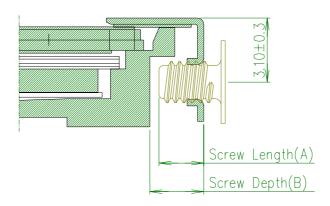
<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



- *Mounting Screw Length (A)
 - = 2.0(Min) / 2.5(Max)
- *Mounting Screw Hole Depth (B) = 2.5(Min)
- *Mounting Hole Location : 3.10(typ.)
- *Torque : 2.0 kgf.cm(Max)

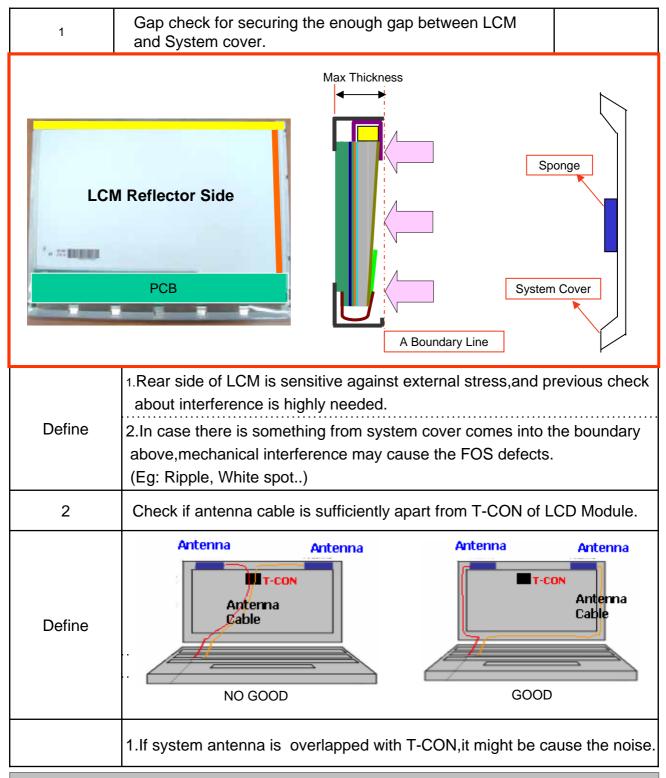
(Measurement gauge : torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

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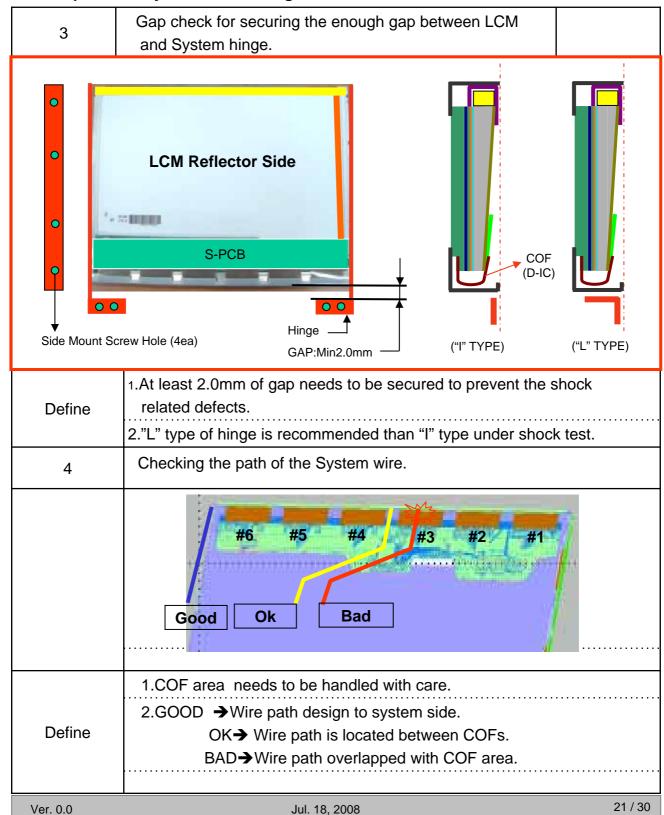
LPL Proposal for system cover design.(Appendix)



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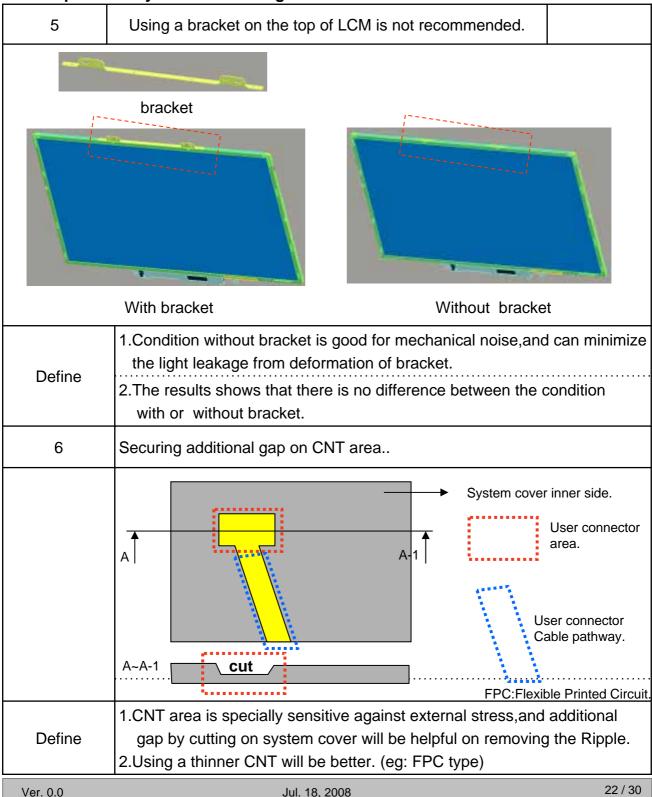


LPL Proposal for system cover design.





LPL Proposal for system cover design.





6. Reliability

Environment test condition

No.	Test Item	Conditions				
1	High temperature storage test	Ta= 60°C, 240h				
2	Low temperature storage test	Ta= -20°C, 240h				
3	High temperature operation test	Ta= 50°C, 50%RH, 240h				
4	Low temperature operation test	Ta= 0°C, 240h				
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis				
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)				
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr				

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J K L	М
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A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 20 pcs

b) Box Size: TBD

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3



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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3



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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3



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